Notice of References Cited	Application/Control No. 10/685,051	Applicant(s)/Patent Under Reexamination TRAUT, ERIC	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,397,242 B1	05-2002	Devine et al.	718/1
*	B	US-7,191,440 B2	03-2007	Cota-Robles et al.	718/1
*	C	US-6,658,657 B1	12-2003	Lueh, Guei-Yuan	717/158
*	D	US-2005/0076186 A1	04-2005	Traut, Eric P.	712/001
*	E	US-2004/0025158 A1	02-2004	Traut, Eric P.	718/1
*	F	US-2005/0080753 A1	04-2005	Vega et al.	706/021
*	G	US-2002/0165848 A1	11-2002	Rautenbach et al.	707/1
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Robin et al., "Analysis of the Intel Pentium's Ability to Support a Secure Virtual Machine Monitor", Aug. 14, 2000, USENIX, pp. 1-17
	V	A. Tamches, "Fine-Grained Dynamic Instrumentation of Commodity Operating System", May, 2001, University of Wisconsin - Madison, pp. 1-213
	W	J. S. Robin, "Analyzing the INTEL Pentium's Capability to Support a Secure Virtual Machine Monitor", Sep., 1999, Naval Postgraduate School, pp. 1-98
	X	K. P. Lawton, "Bochs x86 Emulator - monitor-host.c", March 25, 2000, bochs.sourceforge.net, pp. 1-42

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/685,051	Applicant(s)/Patent Under Reexamination TRAUT, ERIC	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Carlos et al., "User-Kernel Reactive Threads for Linux", Jan. 2003, SCI 2003, pp. 1-6
	V	V86, "Virtual 8088 Mode", 1986, Intel 80386 Programmer's Reference, pp. 1-18
	W	K. Lawton, "Running Multiple Operating Systems Concurrently on an IA32 PC Using Virtualization Techniques", Nov. 1999, pp. 1-44
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.